

APR. 6. 2006 3:57PM

(2)-FISH&RICHARDSON_6175428906

NO. 8182 P. 3

Attorney's Docket No.: 10559-310US1 /INTEL P9631

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RECEIVED
CENTRAL FAX CENTER

APR 06 2006

Applicant : Gilbert Wolrich et al.
Assignee : Intel Corporation
Serial No. : 10/070,091
Filed : February 27, 2002
Title : REGISTER SET USED IN MULTITHREADED PARALLEL PROCESSOR
ARCHITECTURE

Art Unit : 2183
Examiner : Daniel H. Pan

MAIL STOP AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO FINAL ACTION OF FEBRUARY 7, 2006

Please amend the above-identified application as follows:

BEST AVAILABLE COPY

CERTIFICATE OF TRANSMISSION BY FACSIMILE

I hereby certify that this correspondence is being transmitted by facsimile to the Patent and Trademark Office on the date indicated below.

April 6, 2006
Date of Transmission

Signature

Debbie Nass
Typed or Printed Name of Person Signing Certificate